

JAN 05 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: PARK, Woon-Yong; LEE, Won-Hee; KIM Il-Gon; LIM, Seung-Taek;
SONG, You-Lee; and JUN, Sahng-Ik
Assignee: Samsung Electronics Co., Ltd.
Title: THIN FILM TRANSISTOR ARRAY PANEL, MANUFACTURING
METHOD THEREOF, AND MASK THEREFOR
Application No.: 10/771,278 Filing Date: February 2, 2004
Examiner: ANDUJAR, Leonardo Group Art Unit: 2826
Docket No.: AB-1353 US Confirmation No.: 2069

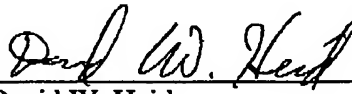
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January 5, 2007

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Dated: January 5, 2007


David W. Heid
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COMMUNICATION

Dear Sir:

This communication is in response to the Notice Requiring Extension of Time Fee mailed November 9, 2006. In view of the official closing of the U.S. Patent and Trademark Office (USPTO) on October 9, 2006 in observance of Columbus Day, Applicants believe that the Response to Restriction Requirement submitted by Express Mail on October 10, 2006 was timely filed. A copy of Express Mail Label No EV 901 346 189 US is attached.

If a fee is required, please charge the fee to Deposit Account No. 50-2257. This paper is being submitted in duplicate.

The Examiner is invited to call the undersigned at (408) 392-9250 with any questions regarding the above-identified application.

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David W. Heid
Attorney(s) for Applicant

Jan 5, 2007
Date

Respectfully submitted,

David W. Heid

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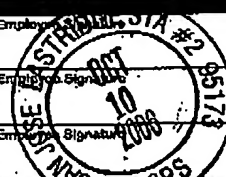
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